深圳市希恩凯电子有限公司 K SHENZHEN CNK ELECTRONIC CO., LTD.							
Produc	ct Specificatio	on For LCD Module					
Model NO. : CNK160160-12002A REVISION : C							
	VAL FOR SPECIF						
APPR	OVAL FOR SPEC	IFICATIONS AND SAMPLE					
CUSTOMER :		APPROVED BY :					
APPROVED BY	CNK LCM R8	D CENTER PREPARED BY					
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RECO	RD OF REVISION		
REV	COMMENT	PAGE	DATE
А	Initial Release	1-16	2012/08/14
В	更改为 FFC 插座及位置,FFC 改为 80mm,两端带补强板	1-16	2013/4/2
С	按键 ,LED 灯和 FFC 连接器插座为客供	1-16	2013/9/11



Weight

TBD

4.	4. GENERAL SPECIFICATION							
	ITEM	CONTENTS						
	Module Size	90(W) \times 90 (H) \times 9.10(T) mm						
	Display View Area	62.3(W) × 62.4(H) mm						
	LCD Type	FSTN/POSITIVE/TRANSFLECTIVE						
	View Angle	6 O'clock						
	Driver IC	UC1698U						
	Backlight Driver type	Power/WHITE						
	DC to DC circuit	Build-In						



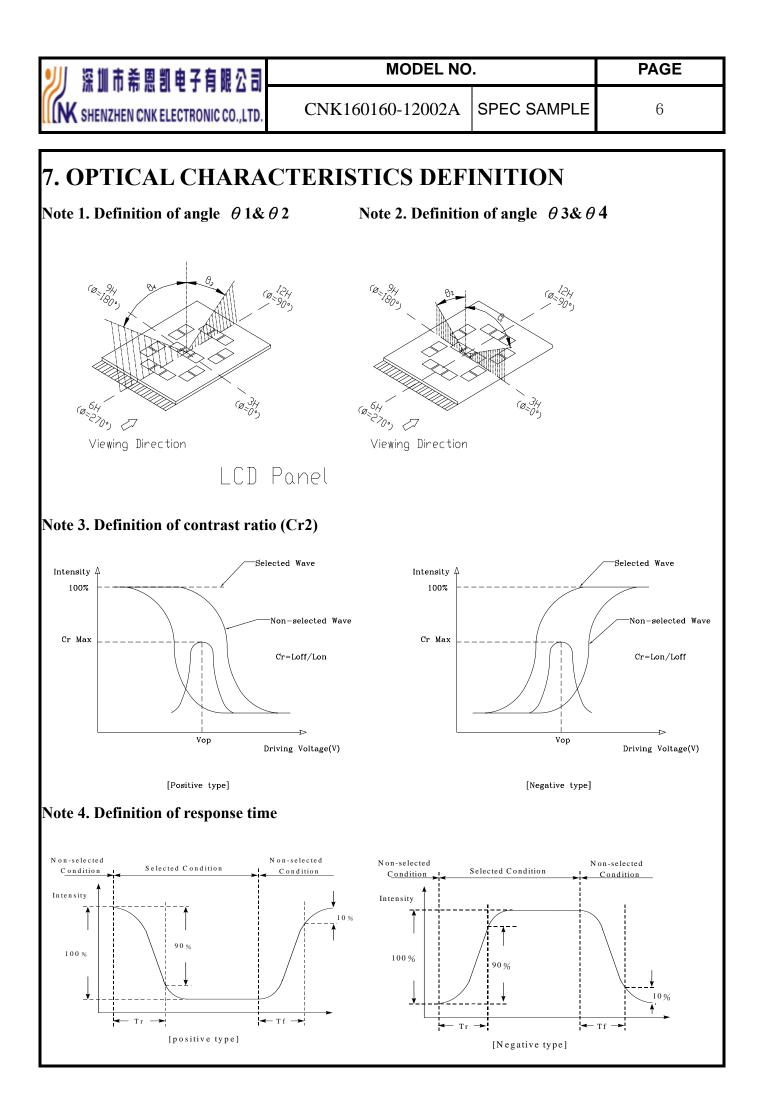
5. LCD ELECTRO-OPTICAL CHARACTERISTICS (Ta=25°C)

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
LCD Module Driving Voltage	VDD	Ta=25 ℃	3.1	3.3	3.5	Volt
Operating Temperature	Тор		-20 ℃	-	+70 ℃	°C
Storage Temperature	Tst		-30 ℃	-	+80 ℃	°C
Humidity	%			90%		

Note: See section 12 for backlight uniformity measurement

6. LCD OPTICAL CHARACTERISTICS

Item		Symbol	Tomn(°C)	Rating			T ⊺:4	D . f		
		Symbol	Temp(℃)	Min	Тур	Max	Unit	Reference		
			50							
Recomm Driving		Vop	25	14.9	15.1	15.3	v			
Driving Voltage			0							
Response Time	Rise Time	Tr	25		180	230	- ms			Natad
	Fall Time	Tf	25		180	230		Note4		
Frame Fr	requency	FR	25	70	75	80	Hz			
	Ø=0°	θ_1			25		- Deg			
Viewing	Ø =180°	θ_2	25		25					
angle Cr≧2	Ø =90°	θ_3	25		15			Note1 Note2		
	Ø=270°	θ_4			35					
Viewing Direction			6 O'clock							
Contras	t Ratio	Cr	25	6	8			Note3		





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8. INTERFACE PIN ASSIGNMENT

PIN	SYMBOL
1	K1
2	K2
3	K3
4	K4
5	K5
6	K6
7	L1
8	L2
9	L3
10	L4
11	VCC
12	VSS
13	VDD
14	/WR
15	/RD
16	/CE
17	C/D
18	RESET
19	DB0
20	DB1
21	DB2
22	DB3
23	DB4
24	DB5
25	DB6
26	DB7
27	A
28	К



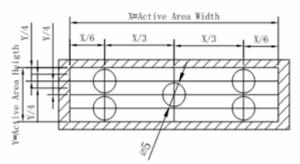
9. BACKLIGHT

BACKLIGHT ELECTRICAL-OPTICAL CHARACTERISTICS (Unless specified, Ambient temperature Ta=25°C)

PARAMETER	Sym.	Min.	Тур.	Max.	Unit	Test Condition	Reference
Supply Current	I	-	60	70	mA	60mA	
WHITE LED	V	2.9	3.1	3.3	v	60mA	
Backlight Luminous Intensity	Lv				Cd/m ²	60mA	Note1
Uniformity		70			%	60mA	Note1 Note2

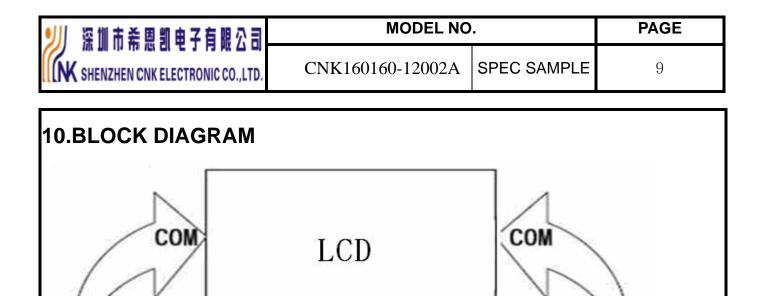
NOTE:

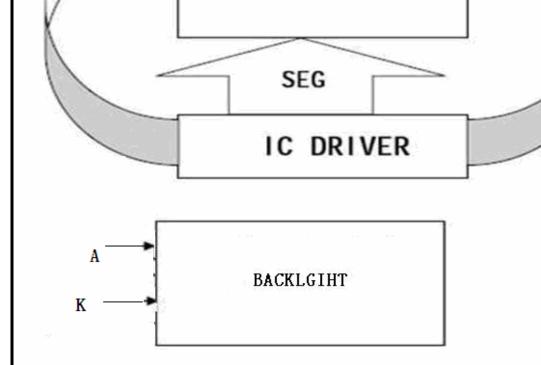
1. Backlight luminance: The measurement instrument is BM-7 luminance colorimeter. The aperture of colorimeter is ø5mm and the distance between lens and backlight is 50cm. 5 points will be measured and the luminance of backlight is the average value of 5 points.



measure point on backlight

2. Backlight Uniformity = (The Luminance min / The Luminance max) x 100%







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 $1 \ 0$

11.AC Characteristics CD t_{AS80} t_{AH80} CS0 CS1 tcssa80 tcsH80 tcy80 tewrso, tewwso t_{HPW80} WR0 WR1 t_{DS80} t_{DH80} Write D[7:0] t_{ACC80} t_{OD80} Read D[7:0] (2.5V ≤ V_{DD} < 3.3V, Ta= -30 to +85°C) Signal Condition Symbol Description Min. Max. Units t_{aseo} CD Address setup time 0 nS t_{AH80} Address hold time nS t_{CY80} System cycle time _ 170 16-bit bus (read) 130 100 (write) 8-bit bus (read) 80 LC[7:6]=10b (write) LC[7:6]=01b 90 WR1 Pulse width 16-bit (read) 85 nS t_{PWR80} -50

LC[7:6]=10b LC[7:6]=01b

LC[7:6]=10b

LC[7:6]=01b

C_L = 100pF

65

40 45

85

65

50 40 45

30

0

-15

5

5

-

-

_

60

30

nS

nS

nS

nS

nS

(1.65V ≤ V_{DD} < 2.5V, Ta= -30 to +85°C)

WR0

WR0, WR1

D0~D15

CS1/CS0

t_{PWW80}

t_{HPW80}

tosao

t_{DH80}

t_{ACC80}

t_{opao}

TCSSA80

t_{CSH80}

Symbol	Signal	Description	Condition	Min.	Max.	Units
taseo taheo	CD	Address setup time Address hold time		0	-	nS
t _{CY80}		System cycle time 16-bit bus (read) (write) 8-bit bus (read) (write)	LC[7:6]=10b LC[7:6]=01b	320 270 180 145 220	-	nS
t _{PWR80}	WR1	Pulse width 16-bit (read) 8-bit		160 90	-	nS
tewwao	WR0	Pulse width 16-bit (write) 8-bit	LC[7:6]=10b LC[7:6]=01b	135 73 110	-	nS
t _{HPW80}	WR0, WR1	High pulse width 16-bit bus (read) (write) 8-bits bus (read) (write)	LC[7:6]=10b LC[7:6]=01b	160 135 90 72 110	-	nS
tosao toнao	D0~D15	Data setup time Data hold time		60 0	-	nS
taccao t _{odao}		Read access time Output disable time	C _L = 100pF	- 30	120 60	nS
t _{CSSA80} t _{CSH80}	CS1/CS0	Chip select setup time		10 10		nS

8-bit

8-bit

(read) (write)

(read)

(write)

Pulse width 16-bit (write)

High pulse width

Data setup time

Data hold time

Read access time

Output disable time

Chip select setup time

16-bit bus

8-bit bus



	No	Test Item	Content of Test	Test
	INU	Test Item	Content of Test	
				Condition
	1	High Temperature	Endurance test of high temperature for a long time.	80°C
		Storage		96H
	2	Low Temperature	Endurance test of low temperature for a long time.	-20±2℃
		Storage		96H
	3	High Temperature	Endurance test of electrical stress (Voltage & Current)	70°C
		Operation	and the thermal stress to the element.	96H
Γest	4	High Temperature	Endurance Test of high temperature and high	45±2℃
ent		/Humidity Storage	humidity for a long time.	90±2%RH
Environment Test				96H
JVird	5	Thermal shock	Endurance test of low and high temperature	-10±2°C/70±2
Ē			cycles.(air to air)	C
			$-20\pm2^{\circ}C \iff 70\pm2^{\circ}C$	10 cycle
			(60min) <i>(</i> 60min)	
			1 cycle	
	6	vibration	Maximum vibration is 2.45m/s2 (0.25 G) during	Ambient
			operation and 11.75 m/s2 (1.2 G) during storage.	temperature
			Tested 10-100KHz XYZ directions 1 hour each.	Ta=25°C
	7	shock	Maximum shock is 29.4 m/s2 (3 G) during operation	Ambient
			and 490.0 m/s2 (50 G) during storage. Tested 10	temperature
			milliseconds in XYZ directions 1 time each.	Ta=25°C

Note:

1) Condensation is not allowed during low temperature testing.

2) Driving condition for operation test:

Power Supply Current for BackLight(ImA)=15mA

Failure Judgment Criterion

After the above mentioned test (For Environmental Test, after 2 hours in room temperature):

1) There should not be conspicuous failure of display quality and appearance.

2) Contrast ratio should be greater than or equal to 50% of the initial contrast ratio.

3) Abnormal function is a failure.



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NO	Item		Criteria					
[Electrical Testing	 (1) non-display (2) segment missing (3) segment short 	 non-display segment missing segment short 					
2	Dimension state	Dimension out of the specification						
		Substrate check symbol Def X: Length direction Y: Short side direction Z: Thickness direction T: Glass thickness K:LCD length L: Single connector width	inition:					
		(1) General crack	X	Y		Z		
		N X I	1/8K≥ Not over viewing area		T≥			
		(2) Corner	X	Y		Z		
3	Glass crack		1/8K≥		ot over wing area	No check	2.50	
		(3) Contact pad crack			T	T	,	
			X 1/3	8K≥	Y 1/3L≥	Z No		
		 Cracks on the contact area cannot exceed 1/2 of the glass thickness. Y not to exceed 1/3 seal width 						
		(4) Substrate protuberance and internal crack						
					D<2/31	L ,Reject		

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NO	Item	Criterion					AQL
4.	Black spot , white spot (including polarizer) $\emptyset = (X+Y)/2$	(1) Round type $\begin{array}{c} & \\ & \\ & \\ & \\ & \\ \end{array}$ (2) Line type $\begin{array}{c} \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ \end{array}$	Size $\emptyset \leqslant 0.1$ $0.10 < \emptyset \leqslant 0.2$ $0.20 < \emptyset \leqslant 0.2$ $0.25 < \emptyset$ Length L accept $3.0 \ge L$ $2.5 \ge L$	20	$\frac{1}{W}$ No check $\frac{W}{2}$		1.50
	unit:mm	(3) No more than 2 spots and lines within 3 mm. Maximum combined total of round and line defects is 4. (4) Scratches criterion is same as that of Round type.					
5.	Pixel deformation	Symbols: W: segment width \emptyset : average of diameter =(A+B)/2 (1)Pin hole and deformation $\begin{array}{c} & & \\ \hline \hline & & \\ \hline \hline & & \\ \hline \hline & & \\ \hline & & \\ \hline & & \\ \hline & & \\ \hline \hline & & \\ \hline & & \\ \hline & & \\ \hline & & \\ \hline \hline & & \\ \hline & & \\ \hline & & \\ \hline & & \\ \hline \hline \hline & & \\ \hline \hline \hline & & \\ \hline \hline \hline \hline$					2.5
6.	Polarizer bubble Ø=(X+Y)/2		$\emptyset \leqslant 0.20 < \emptyset$ $0.50 < \emptyset$ $1.00 < \emptyset$	0≤1.00	Acceptable QTY No check 3 2 0 3		1.5
7.	Contrast	Under normal pov	Under normal power supply, uneven contrast is unacceptable.				
8.	Rainbow	Obvious uneven color in LCD viewing area is not allowed.			2.5		

14. PRECAUTION FOR USE OF LCD MODULE

1. Handling Precautions

- 1) The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.
- 2) If the display panel is damaged, the liquid crystal substance leaks out ,do not ingest. If the substance contacts skin or clothes, promptly wash off using soap and water.
- 3) Do not apply excessive force to the display surface or adjoining areas since this may affect the LCD color
- 4) The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.
- 5) If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:
 - --Isopropyl alcohol
 - --Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer.

Especially, do not use the following:

--Water

--Ketone

--Aromatic solvents

6) Do not attempt to disassemble or process the LCD module.

2. Assembling Precautions

- When mounting the LCD module make sure that it is free of twisting, warping, and distortion. Distortion has great influence upon display quality. Also, use an adequately stiff outer case.
- 2) Please handle the LCD module by its side.
- 3) NC terminal should be open. Do not connect anything.
- 4) If the logic circuit power is OFF, do not apply the input signals.
- 5) To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.

•Be sure to ground the body when handling the LCD module.

·Tools required for assembly, such as soldering irons, must be properly grounded.

•To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.

•The LCD module is coated with a film to protect the display surface. Exercise care when peeling off this protective film since static electricity may be generated.

6) Be careful handling the glass panel because it has a very sharp edge.

3. Storage Precautions

1) When storing the LCD module, avoid exposure to direct sunlight, to the light of fluorescent lamps,

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to high temperature or to high humidity. Whenever possible, LCD modules should be stored in the same packaging they were shipped in.

2) Exercise care to minimize corrosion of the electrodes. Corrosion of the electrodes is accelerated by water droplets or by current flow in a high-humidity environment.

4. Design Precautions

- The absolute maximum ratings represent the rated value beyond which LCD module can not exceed. When the LCD modules are used in excess of this rated value, their operation characteristics may be adversely affected.
- To prevent the occurrence of erroneous operation caused by noise, attention must be paid to satisfy V_{IL}, V_{IH} specification values including taking the precaution of using signal cables that are short.
- 3) The LCD exhibits temperature dependency characteristics. Since recognition of the display becomes difficult when the LCD is used outside its designated operating temperature range, be sure to use the LCD within this range. Also keep in mind that the LCD driving voltage levels necessary for clear displays will vary according to temperature.
- 4) We recommended that power supply lines (VDD) have over-current protection line. (Fuse etc. Recommend Value:0.5A)
- 5) Sufficiently reduce electrical noise from peripheral devices.
- 6) To cope with EMI, take measures basically on outputting side.
- 7) Assemble LCD module tightly with the application case or PCB.

5. Other considerations

- 1) Liquid crystal solidifies under low temperature (below the storage temperature range) leading to defective orientation or the generation of air bubbles (black or white). Air bubbles may also be generated if the LCD module is subjected to a strong shock at a low temperature.
- 2) If the LCD modules have been operating for a long time showing the same display patterns, the display patterns may remain on the screen as ghost images and a slight contrast irregularity may also appear. A normal operating status can be regained by suspending use for some time. It should be noted that this phenomenon does not adversely affect performance reliability.
- 3) To minimize the performance degradation of the LCD module's resulting from destruction caused by static electricity, etc., exercise care to avoid touching the LCD's electrical connections.
- 4) LCD voltage adjustment may be necessary to obtain the best contrast on each LCD.
- 5) Precaution for disposal of LCD module. When disposal of LCD module, ask specialization company of industrial waste which is permitted by the government. When burn up LCD module, obey the law of environmental hygienic.

