深圳市希恩凯电子有限公司

SHENZHEN CNK ELECTRONIC CO.,LTD.

Product Specification For LCD Module

Model NO.：CNK16064-13001A

REVISION：A

☐APPROVAL FOR SPECIFICATIONS ONLY

■**APPROVAL FOR SPECIFICATIONS AND SAMPLE**

CUSTOMER：	APPROVED BY：

CNK LCM R&D CENTER		
APPROVED BY	**CHECKED BY**	**PREPARED BY**
DIRECTOR	**MANAGER**	**Engineer**

深圳市希恩凯电子有限公司
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3. RECORD OF REVISION

REV	COMMENT	PAGE	DATE
A	Initial Release	1-16	2013/04/17

4. GENERAL SPECIFICATION

ITEM	CONTENTS
Module Size	78.7(W) ×35.9 (H) ×2.8(T) mm
Display View Area	75.3(W) × 28.3(H) mm
LCD Type	STN/GRAY/REFLECTIVE
View Angle	6 O'clock
Driver IC	IST3020
DC to DC circuit	Build-In
Weight	TBD

5. LCD ELECTRO-OPTICAL CHARACTERISTICS (Ta＝25℃)

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
LCM Module Driving Voltage	VDD	Ta＝25℃	2.8	3.0	3.3	Volt
Operating Temperature	Top	--	-10℃	-	+60℃	℃
Storage Temperature	Tst	--	-20℃	-	+70℃	℃
Humidity	%	--	--	90%		

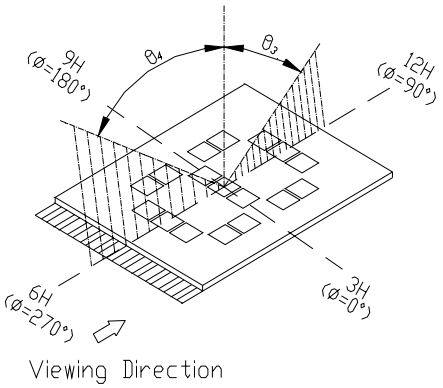
Note: See section 12 for backlight uniformity measurement

6. LCD OPTICAL CHARACTERISTICS

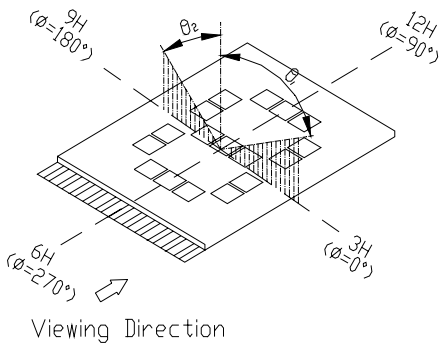
Item		Symbol	Temp(℃)	Rating			Unit	Reference
				Min	Typ	Max		
Recommended Driving Voltage		Vop	50				V	
			25	8.8	9.0	9.2		
			0					
Response Time	Rise Time	Tr	25		180	230	ms	Note4
	Fall Time	Tf	25		180	230		
Frame Frequency		FR	25	70	75	80	Hz	
Viewing angle Cr≧2	Ø=0°	θ_1	25		25		Deg	Note1 Note2
	Ø =180°	θ_2			25			
	Ø =90°	θ_3			15			
	Ø =270°	θ_4			35			
Viewing Direction			6 O'clock					
Contrast Ratio		Cr	25	6	8			Note3

7. OPTICAL CHARACTERISTICS DEFINITION

Note 1. Definition of angle θ1& θ2

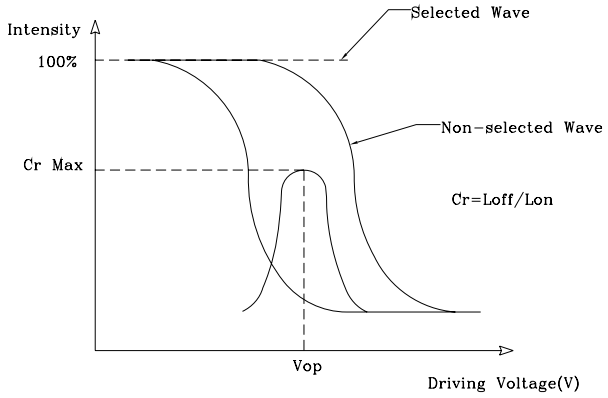


Note 2. Definition of angle θ3& θ4

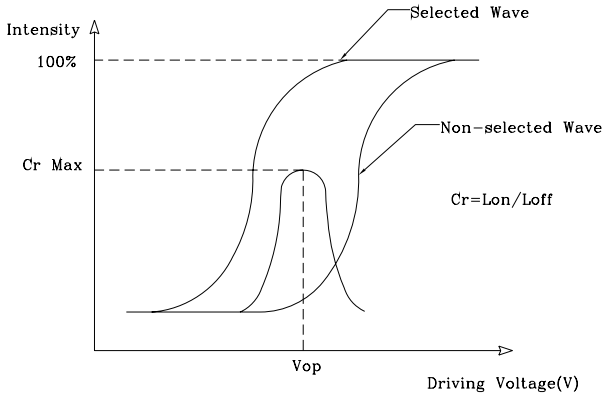


LCD Panel

Note 3. Definition of contrast ratio (Cr2)

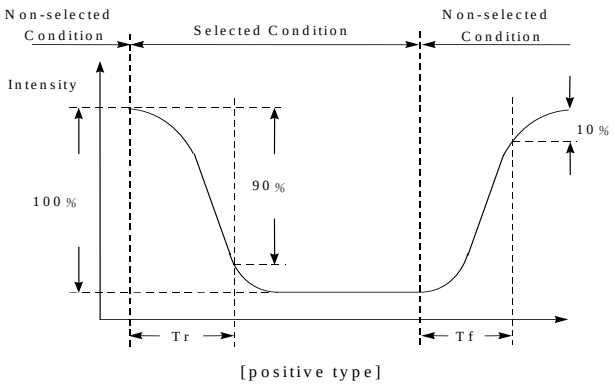


[Positive type]

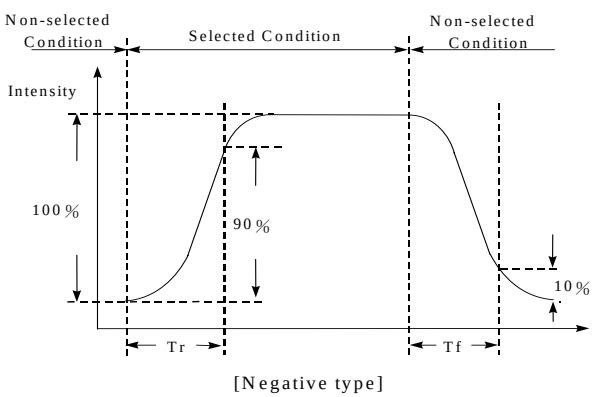


[Negative type]

Note 4. Definition of response time



[positive type]

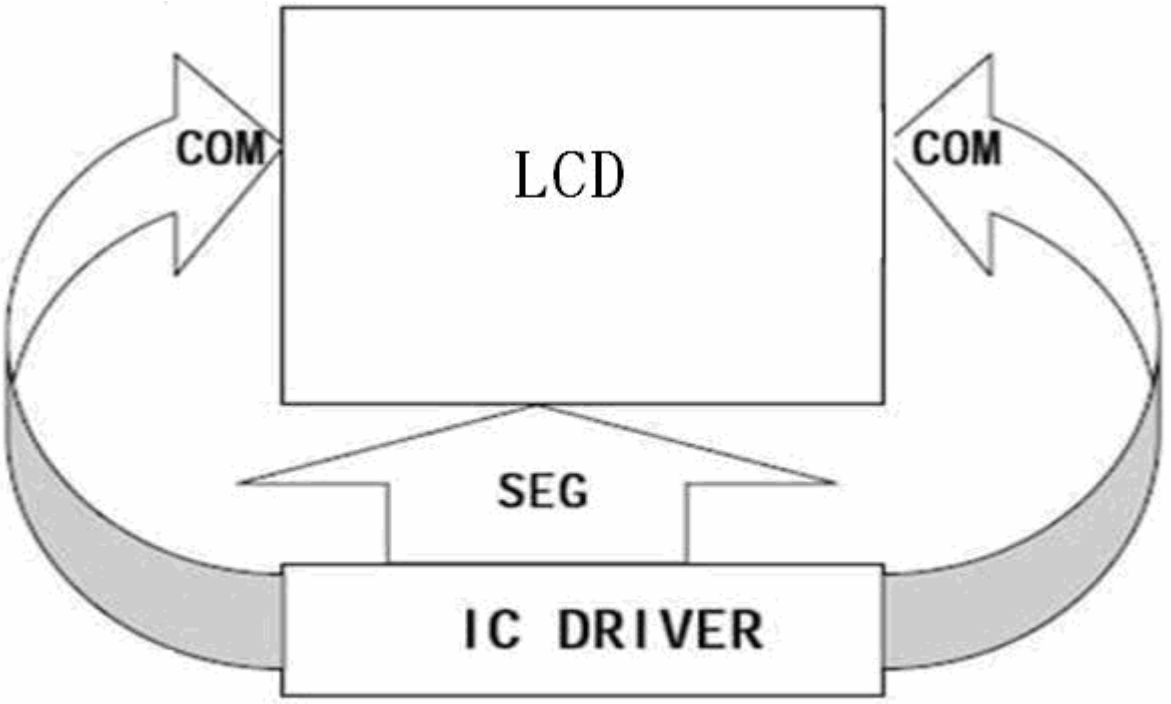


[Negative type]

8. INTERFACE PIN ASSIGNMENT

PIN	SYMBOL	FUNCTIONS
1	VR	Voltage adjustment pad.Applies voltage between vo and VSS using a resistive divider
2	IRS	RS="H":use the internal resistors IRS="L";D0 not use the internal resistors .the vo voltage level is regulated by an external resistive voltage divider attached to the VR terminal
3	V4	LCD driver supplies voltages. The voltage determined by theLCD cell is impedance-converted by a resistive driver or an operation amplifier for application. Voltages should be according to the following relationship:V0 ≥ V1 ≥ V2 ≥ V3 ≥ V4 ≥ VSS2 When the on-chip operating power circuit is on, the following voltages are supplied to V1 to V4 by the on-chip power circuit.
4	V3	
5	V2	
6	V1	
7	V0	
8	C2-	DC/DC voltage converter.connect a capacitor between this terminal and the CAP2Pterminal
9	C2+	DC/DC voltage converter.connect a capacitor between this terminal and the CAP2N terminal
10	C1+	DC/DC voltage converter.connect a capacitor between this erminal and the CAP1N terminal
11	C1-	DC/DC voltage converter.connect a capacitor between this terminal and the CAP1P terminal
12	C3+	DC/DC voltage converter.connect a capacitor between this terminal and the CAP3N terminal
13	VOUT	DC/DC voltage converter output
14	VSS	Ground
15	VDD	Power supply for logic
16	SDA	Serial data input
17	SCK	Serial clock input
18	RS	H:Data L:Instruction code
19	RST	Control registers are re-initialized by their default states
20	CS	This is the chip select signal ,when CS=”L”,

9.BLOCK DIAGRAM



10.AC Characteristics

Read / Write Characteristics (8080-series MPU)

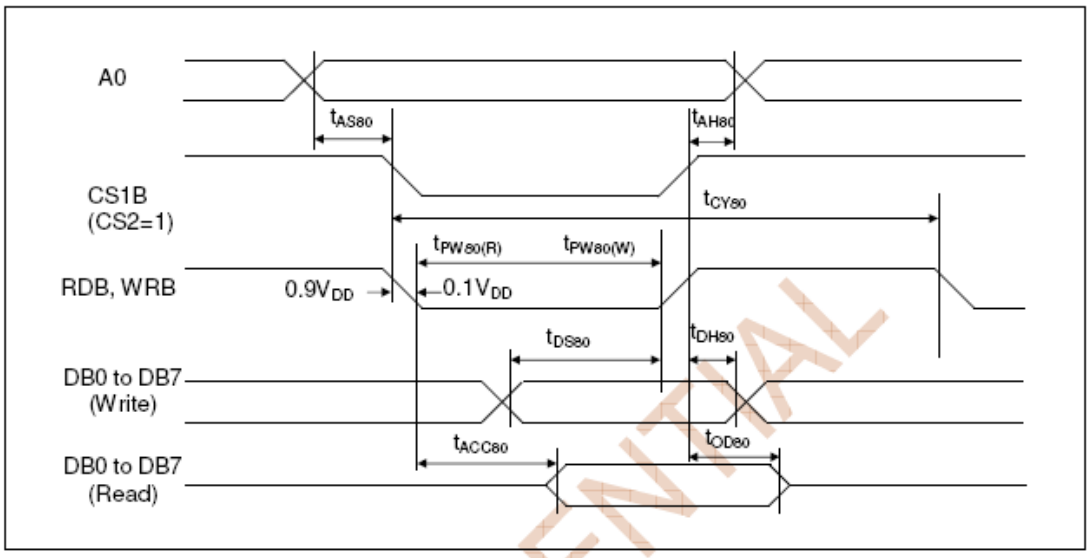


Figure 26. Read / Write Characteristics (8080-series MPU)

(V_{DD} = 2.4 to 3.6V, Ta = -40 to +85°C)

Item	Signal	Symbol	Min.	Typ.	Max.	Unit	Remark
Address setup time Address hold time	A0	t_{AS80} t_{AH80}	0 0	-	-	ns	
System cycle time	A0	t_{CY80}	300	-	-	ns	
Pulse width (WRB)	WRB	t_{PW80}(W)	60	-	-	ns	
Pulse width (RDB)	RDB	t_{PW80}(R)	60	-	-	ns	
Data setup time Data hold time	DB7 to DB0	t_{DS80} t_{DH80}	40 15	-	-	ns	
Read access time Output disable time		t_{ACC80} t_{OD80}	- 10	-	140 100	ns	C_L = 100pF

Read / Write Characteristics (6800-series Microprocessor)

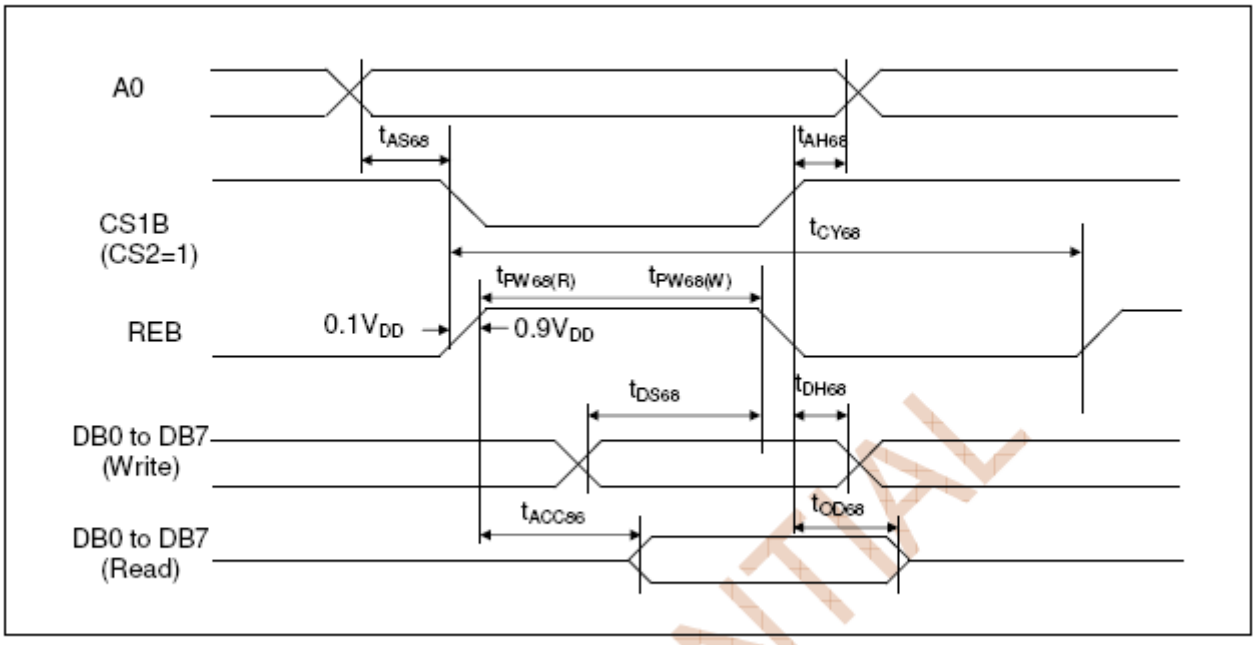


Figure 27. Read / Write Characteristics (6800-series Microprocessor)

(V_{DD} = 2.4 to 3.6V, Ta = -40 to +85℃)

Item		Signal	Symbol	Min.	Typ.	Max.	Unit	Remark
Address setup time Address hold time		A0	tAS68 tAH68	0 0	-	-	ns	
System cycle time		A0	tCY68	300	-	-	ns	
Data setup time Data hold time		DB7 to DB0	tDS68 tDH68	40 15	-	-	ns	
Access time Output disable time			tACC86 tOD68	- 10	-	140 100	ns	CL = 100pF
Enable pulse width	Read Write	RDB	tPW68(R) tPW68(W)	60	-	-	-	

11. RELIABILITY

	No	Test Item	Content of Test	Test Condition
Environment Test	1	**High Temperature Storage**	**Endurance test of high temperature for a long time.**	**80℃ 96H**
	2	**Low Temperature Storage**	**Endurance test of low temperature for a long time.**	**-20±2℃ 96H**
	3	**High Temperature Operation**	**Endurance test of electrical stress (Voltage & Current) and the thermal stress to the element.**	**70℃ 96H**
	4	**High Temperature /Humidity Storage**	**Endurance Test of high temperature and high humidity for a long time.**	**45±2℃ 90±2%RH 96H**
	5	**Thermal shock**	**Endurance test of low and high temperature cycles.(air to air)** **-20±2℃ ⟷ 70±2℃** **(60min) ⟷ (60min)** **1 cycle**	**-10±2℃/70±2℃ 10 cycle**
	6	**vibration**	**Maximum vibration is 2.45m/s2 (0.25 G) during operation and 11.75 m/s2 (1.2 G) during storage. Tested 10-100KHz XYZ directions 1 hour each.**	**Ambient temperature Ta=25°C**
	7	**shock**	**Maximum shock is 29.4 m/s2 (3 G) during operation and 490.0 m/s2 (50 G) during storage. Tested 10 milliseconds in XYZ directions 1 time each.**	**Ambient temperature Ta=25°C**

Note:

1) Condensation is not allowed during low temperature testing.

2) Driving condition for operation test:

Power Supply Current for BackLight(ImA)=15mA

Failure Judgment Criterion

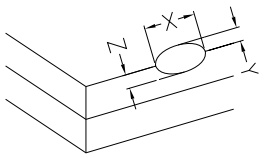
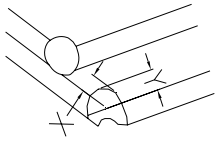
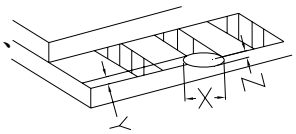
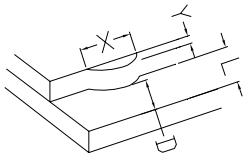
After the above mentioned test (For Environmental Test, after 2 hours in room temperature):

1) There should not be conspicuous failure of display quality and appearance.

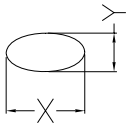
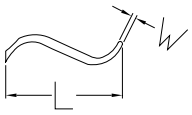
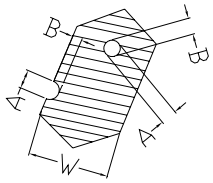
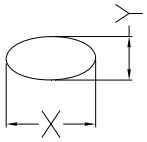
2) Contrast ratio should be greater than or equal to 50% of the initial contrast ratio.

3) Abnormal function is a failure.

12. INSPECTION CRITERIA

<table>
<tr><th>NO</th><th>Item</th><th>Criteria</th><th>AQL</th></tr>
<tr><td>1</td><td>Electrical Testing</td><td>(1) non-display
(2) segment missing
(3) segment short</td><td>0.65</td></tr>
<tr><td>2</td><td>Dimension state</td><td>Dimension out of the specification</td><td>1.00</td></tr>
<tr><td>3</td><td>Glass crack</td><td>Substrate check symbol Definition:
X: Length direction
Y: Short side direction
Z: Thickness direction
T: Glass thickness
K:LCD length
L: Single connector width
(1) General crack
 
<table>
<tr><td>X</td><td>Y</td><td>Z</td></tr>
<tr><td>1/8K≥</td><td>Not over viewing area</td><td>T≥</td></tr>
</table>
(2) Corner
 
<table>
<tr><td>X</td><td>Y</td><td>Z</td></tr>
<tr><td>1/8K≥</td><td>Not over viewing area</td><td>No check</td></tr>
</table>
(3) Contact pad crack
 
<table>
<tr><td>X</td><td>Y</td><td>Z</td></tr>
<tr><td>1/8K≥</td><td>1/3L≥</td><td>No check</td></tr>
</table>
1. Cracks on the contact area cannot exceed 1/2 of the glass thickness.
2. Y not to exceed 1/3 seal width
(4) Substrate protuberance and internal crack
 
D＜2/3L ,Reject
(5) No progressive glass cracks allowed</td><td>2.50</td></tr>
</table>

<table>
<tr><th>NO</th><th>Item</th><th>Criterion</th><th>AQL</th></tr>
<tr><td rowspan="4">4.</td><td rowspan="4">Black spot , white spot (including polarizer)
Ø=(X+Y)/2

unit:mm</td><td>(1) Round type
 
<table>
<tr><th>Size</th><th>Acceptable QTY</th></tr>
<tr><td>Ø≤0. 10</td><td>Accept</td></tr>
<tr><td>0.10＜Ø≤0.20</td><td>2</td></tr>
<tr><td>0.20＜Ø≤0.25</td><td>1</td></tr>
<tr><td>0.25＜Ø</td><td>0</td></tr>
</table></td><td rowspan="4">1.50</td></tr>
<tr><td>(2) Line type
 
<table>
<tr><th>Length L</th><th>Width W</th><th>Acceptable QTY</th></tr>
<tr><td>accept</td><td>0.015≥W</td><td>No check</td></tr>
<tr><td>3.0≥L</td><td>0.050≥W</td><td rowspan="2">2</td></tr>
<tr><td>2.5≥L</td><td>0.080≥W</td></tr>
<tr><td></td><td>0.100＜W</td><td>As round type</td></tr>
</table></td></tr>
<tr><td>(3) No more than 2 spots and lines within 3 mm. Maximum combined total of round and line defects is 4.</td></tr>
<tr><td>(4) Scratches criterion is same as that of Round type.</td></tr>
<tr><td>5.</td><td>Pixel deformation</td><td>Symbols:
W: segment width
Ø: average of diameter ＝(A＋B) / 2
(1)Pin hole and deformation
 
<table>
<tr><th>Width</th><th>Acceptable Defect</th></tr>
<tr><td>W＜0.4</td><td>Ø≤0. 20 and Ø≤1/2W</td></tr>
<tr><td>W≥0.4</td><td>Ø≤0. 25 and Ø≤1/3W</td></tr>
</table>
Ø under 0.10mm ,acceptable
（2) Pixel size should be in the range of 95% to 100% of the normal dimension and the gap between pixels should be less than 150% of normal dimension.</td><td>2.5</td></tr>
<tr><td>6.</td><td>Polarizer bubble
Ø=(X+Y)/2</td><td> 
<table>
<tr><th>size Ø</th><th>Acceptable QTY</th></tr>
<tr><td>Ø≤0. 20</td><td>No check</td></tr>
<tr><td>0.20＜Ø≤0.50</td><td>3</td></tr>
<tr><td>0.50＜Ø≤1.00</td><td>2</td></tr>
<tr><td>1.00＜Ø</td><td>0</td></tr>
<tr><td>Total QTY</td><td>3</td></tr>
</table></td><td>1.5</td></tr>
<tr><td>7.</td><td>Contrast</td><td>Under normal power supply, uneven contrast is unacceptable.</td><td>2.5</td></tr>
<tr><td>8.</td><td>Rainbow</td><td>Obvious uneven color in LCD viewing area is not allowed.</td><td>2.5</td></tr>
</table>

13. PRECAUTION FOR USE OF LCD MODULE

1. Handling Precautions

1) The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.

2) If the display panel is damaged, the liquid crystal substance leaks out ,do not ingest. If the substance contacts skin or clothes, promptly wash off using soap and water.

3) Do not apply excessive force to the display surface or adjoining areas since this may affect the LCD color

4) The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.

5) If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:

--Isopropyl alcohol

--Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer.

Especially, do not use the following:

--Water

--Ketone

--Aromatic solvents

6) Do not attempt to disassemble or process the LCD module.

2. Assembling Precautions

1) When mounting the LCD module make sure that it is free of twisting, warping, and distortion. Distortion has great influence upon display quality. Also, use an adequately stiff outer case.

2) Please handle the LCD module by its side.

3) NC terminal should be open. Do not connect anything.

4) If the logic circuit power is OFF, do not apply the input signals.

5) To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.

·Be sure to ground the body when handling the LCD module.

·Tools required for assembly, such as soldering irons, must be properly grounded.

·To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.

·The LCD module is coated with a film to protect the display surface. Exercise care when peeling off this protective film since static electricity may be generated.

6) Be careful handling the glass panel because it has a very sharp edge.

3. Storage Precautions

1) When storing the LCD module, avoid exposure to direct sunlight, to the light of fluorescent lamps, to high temperature or to high humidity. Whenever possible, LCD modules should be stored in the same packaging they were shipped in.
2) Exercise care to minimize corrosion of the electrodes. Corrosion of the electrodes is accelerated by water droplets or by current flow in a high-humidity environment.

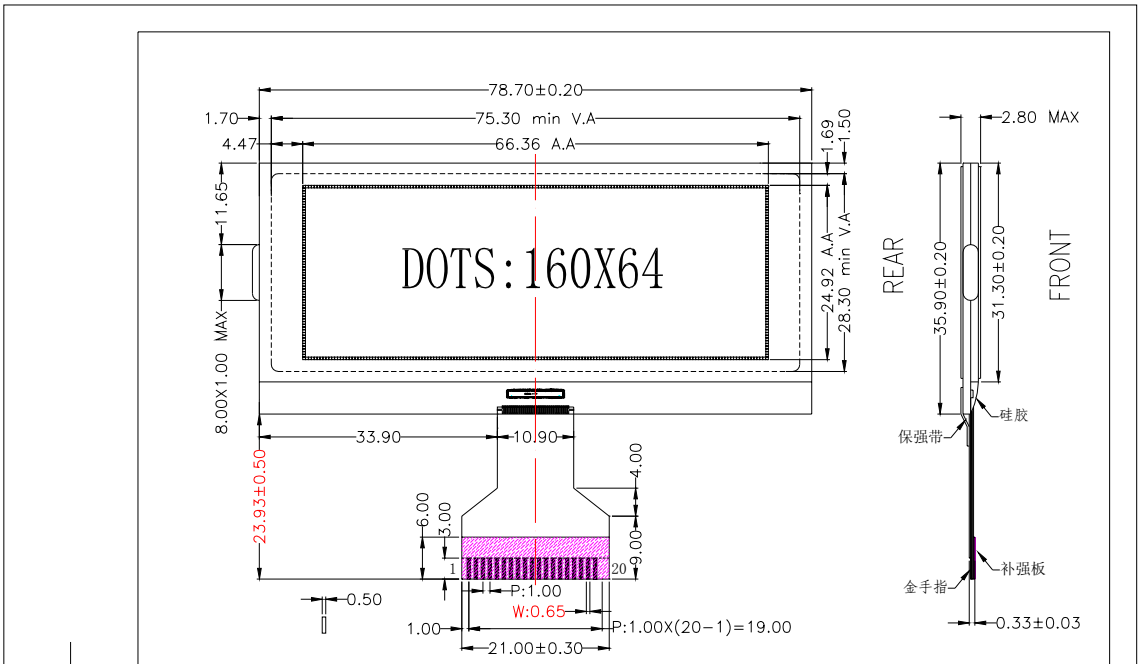
4. Design Precautions

1) The absolute maximum ratings represent the rated value beyond which LCD module can not exceed. When the LCD modules are used in excess of this rated value, their operation characteristics may be adversely affected.
2) To prevent the occurrence of erroneous operation caused by noise, attention must be paid to satisfy V_{IL}, V_{IH} specification values including taking the precaution of using signal cables that are short.
3) The LCD exhibits temperature dependency characteristics. Since recognition of the display becomes difficult when the LCD is used outside its designated operating temperature range, be sure to use the LCD within this range. Also keep in mind that the LCD driving voltage levels necessary for clear displays will vary according to temperature.
4) We recommended that power supply lines (VDD) have over-current protection line. (Fuse etc. Recommend Value:0.5A)
5) Sufficiently reduce electrical noise from peripheral devices.
6) To cope with EMI, take measures basically on outputting side.
7) Assemble LCD module tightly with the application case or PCB.

5. Other considerations

1) Liquid crystal solidifies under low temperature (below the storage temperature range) leading to defective orientation or the generation of air bubbles (black or white). Air bubbles may also be generated if the LCD module is subjected to a strong shock at a low temperature.
2) If the LCD modules have been operating for a long time showing the same display patterns, the display patterns may remain on the screen as ghost images and a slight contrast irregularity may also appear. A normal operating status can be regained by suspending use for some time. It should be noted that this phenomenon does not adversely affect performance reliability.
3) To minimize the performance degradation of the LCD module's resulting from destruction caused by static electricity, etc., exercise care to avoid touching the LCD's electrical connections.
4) LCD voltage adjustment may be necessary to obtain the best contrast on each LCD.
5) Precaution for disposal of LCD module. When disposal of LCD module, ask specialization company of industrial waste which is permitted by the government. When burn up LCD module, obey the law of environmental hygienic.

14.LCM DRAWING

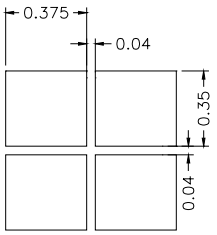


接口定义:

1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
VR	IRS	V4	V3	V2	V1	V0	C2-	C2+	C1+	C1-	C3+	VOUT	VSS	VDD	SDA	SCK	RS	RST	CS

请仔细确认接口定义

DISPLAY TYPE: STN/GRAY
POLARIZ :REFLECTIVE
VIEWING DIRECTION: 6:00-CLOCK
DRIVE METHOD: 1/64DUTY,1/9BIAS
LCD OPERATING VOLTAGE: 9.0v
LCM OPERATING VOLTAGE: 3.0v
OPERATING TEMP: -10 TO 60 Deg.C
STORAGE TEMP: -20 TO 70 Deg.C
CONNECTOR: COG
UNSIGNED TOLERANCE: ±0.20



镀 涂					
旧底图总号			CNK16064-13001A-COG		
底图总号	序号	更改内容	日 期	等 级	标 记	重 量	比 例
	设 计						1.3:1
	审 核						
日 期	签 名			共 1 张	第 1 张	
		批 准		深圳市希恩凯电子有限公司			
格 式 (1)	制图:		描图:	幅面:			